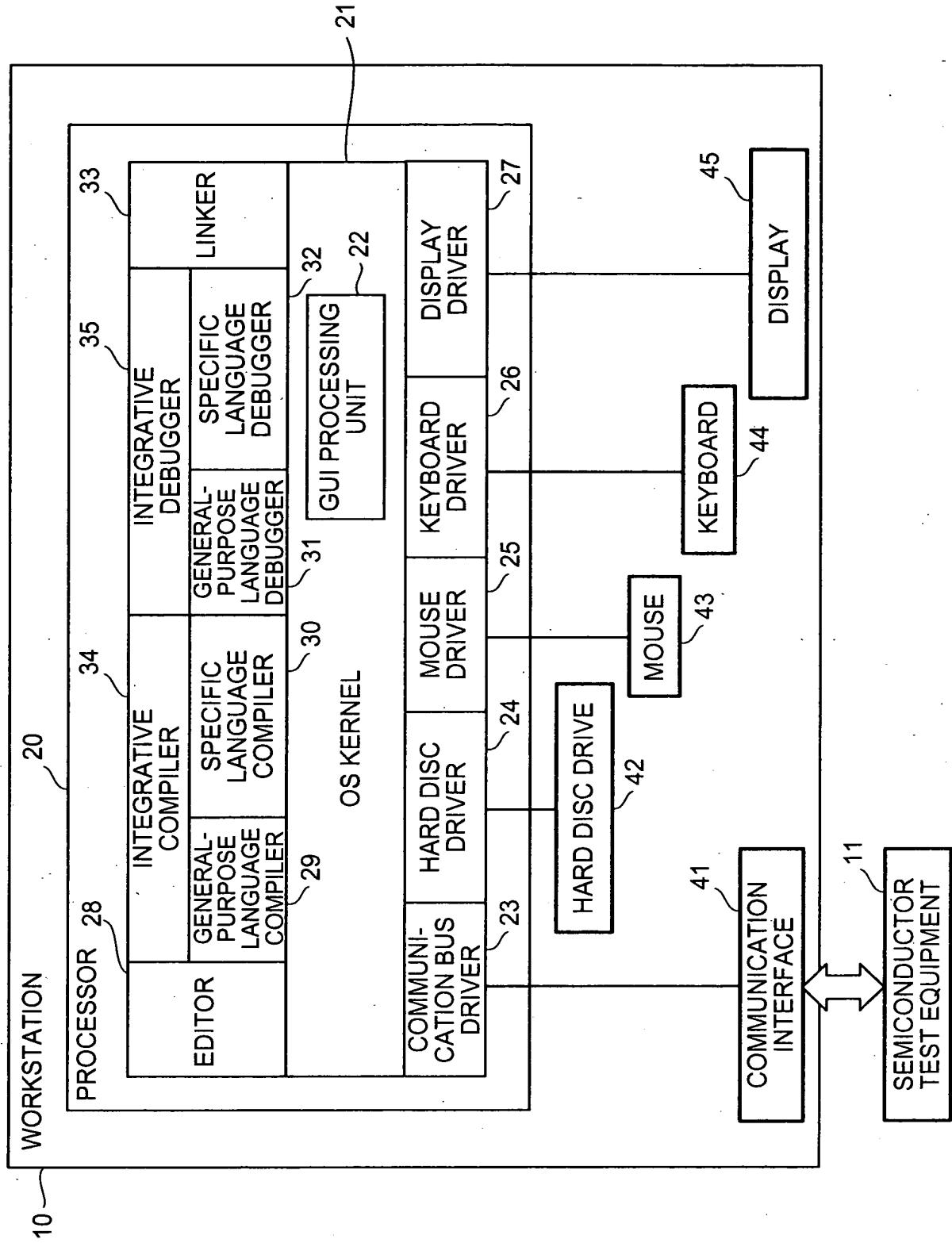


FIG. 1



10/531738

2/12

FIG.2

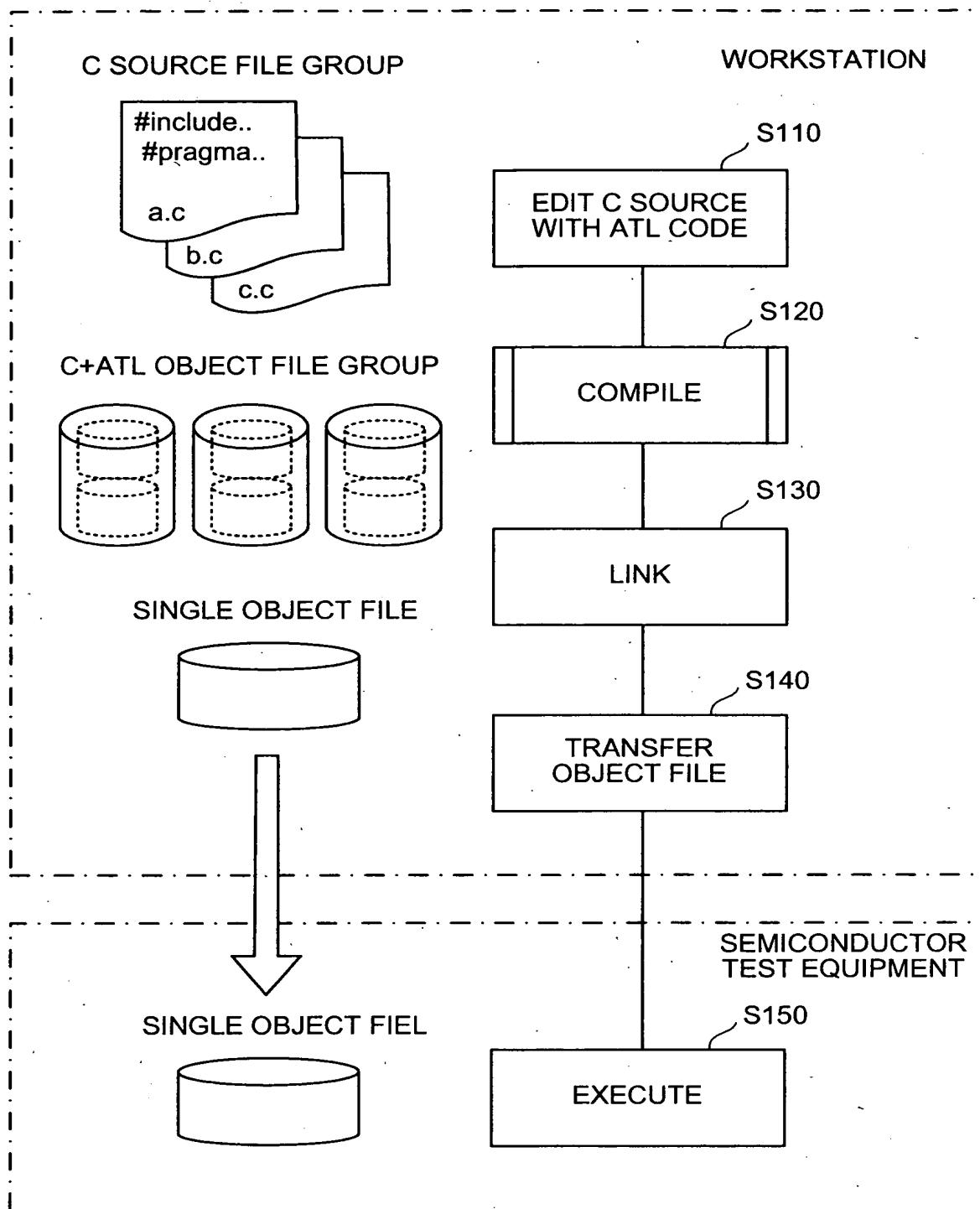


FIG.3

```
1:  #include "AT/hybrid.h"
2:
3:  #pragma atl name      "SAMPLE"
4:  #pragma atl socket    "SSOC"
5:  #pragma atl proc P1(ARG1,ARG2(2)) "{"
6:      WRITE "ARG1=",ARG1,/
7:      WRITE "ARG2=",ARG2,/
8:  }"
9:
10: int test_main(void)
11: {
12:     int idata;
13:     struct atreal rdata[2];
14:
15:     if(ATL_init2(EM_ATL) != 0){
16:         AT_perror("ATL_init2()");
17:         exit(1);
18:     }
19:
20:     idata = 1;
21:     AT_strtorm(rdata, 2, "0.5V", "1.2V");
22:     if(ATL_proc(EM_ATL, "P1", INTE(&idata), DREAL(rdata, 2)) != 0){
23:         AT_perror("P2");
24:         exit(1);
25:     }
26:
27:     return(0);
28: }
```

FIG.4

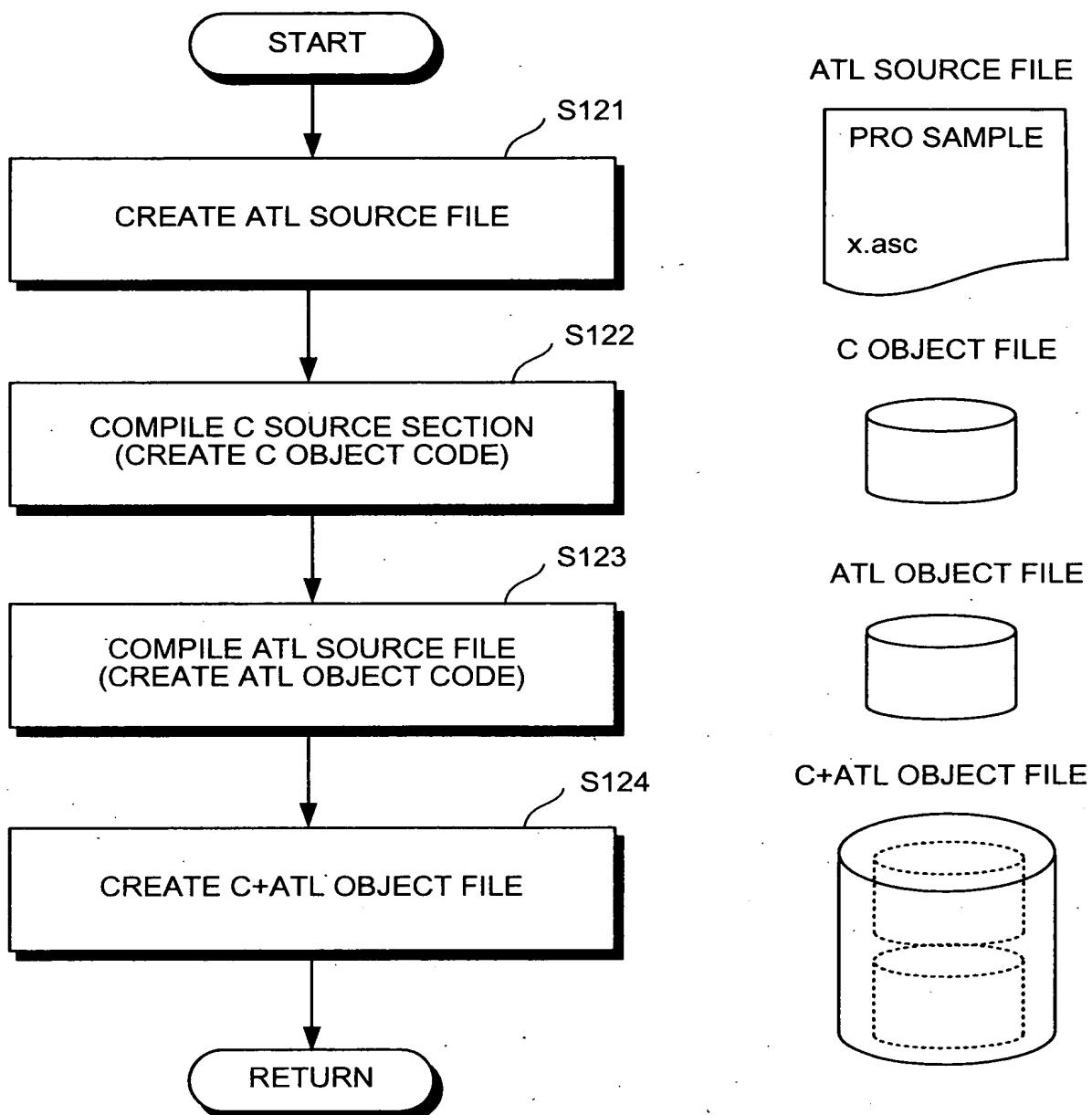


FIG.5

C+ATL SOURCE

```
1:  #include "AT/hybrid.h"
2:
3:  #pragma atl name  "SAMPLE"
4:  #pragma atl socket "SSOC"
5:  #pragma atl proc P1(ARGV1,ARGV2(2)) "{"
6:      WRITE "ARGV1=",ARGV1,/
7:      WRITE "ARGV2=",ARGV2,/
8:  }"
9:
10: int test_main(void)
11: {
12:     :
13:     :
14:     :
15:     :
16:     :
17:     :
18:     :
19: }
```

ATL SOURCE

```
1:  PRO SAMPLE SSOC
2:  P1:ARGUMENT (ARGV1,ARGV2(2))
3:  WRITE "ARGV1=",ARGV1,/
4:  WRITE "ARGV2=",ARGV2,/
5:  GOTO CONTINUE
6:  END
```

FIG.6

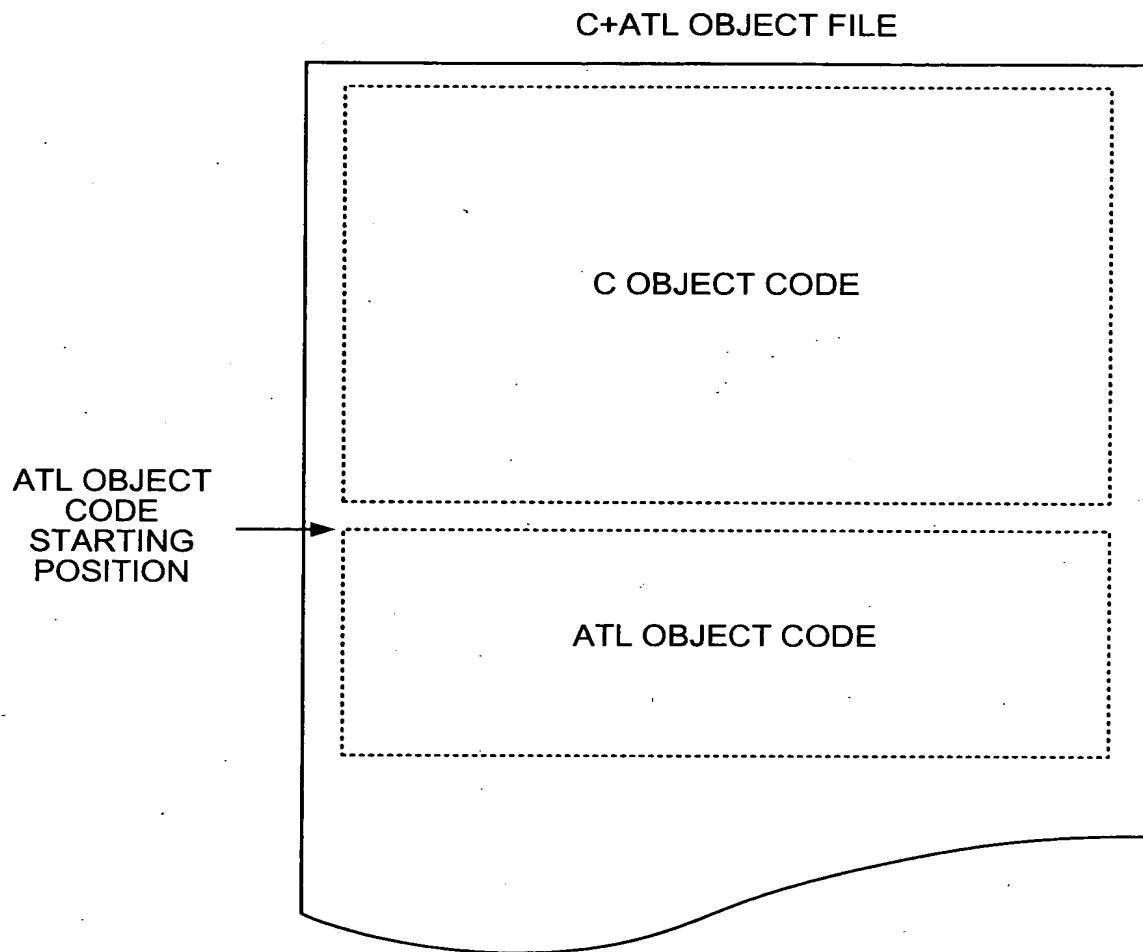


FIG.7

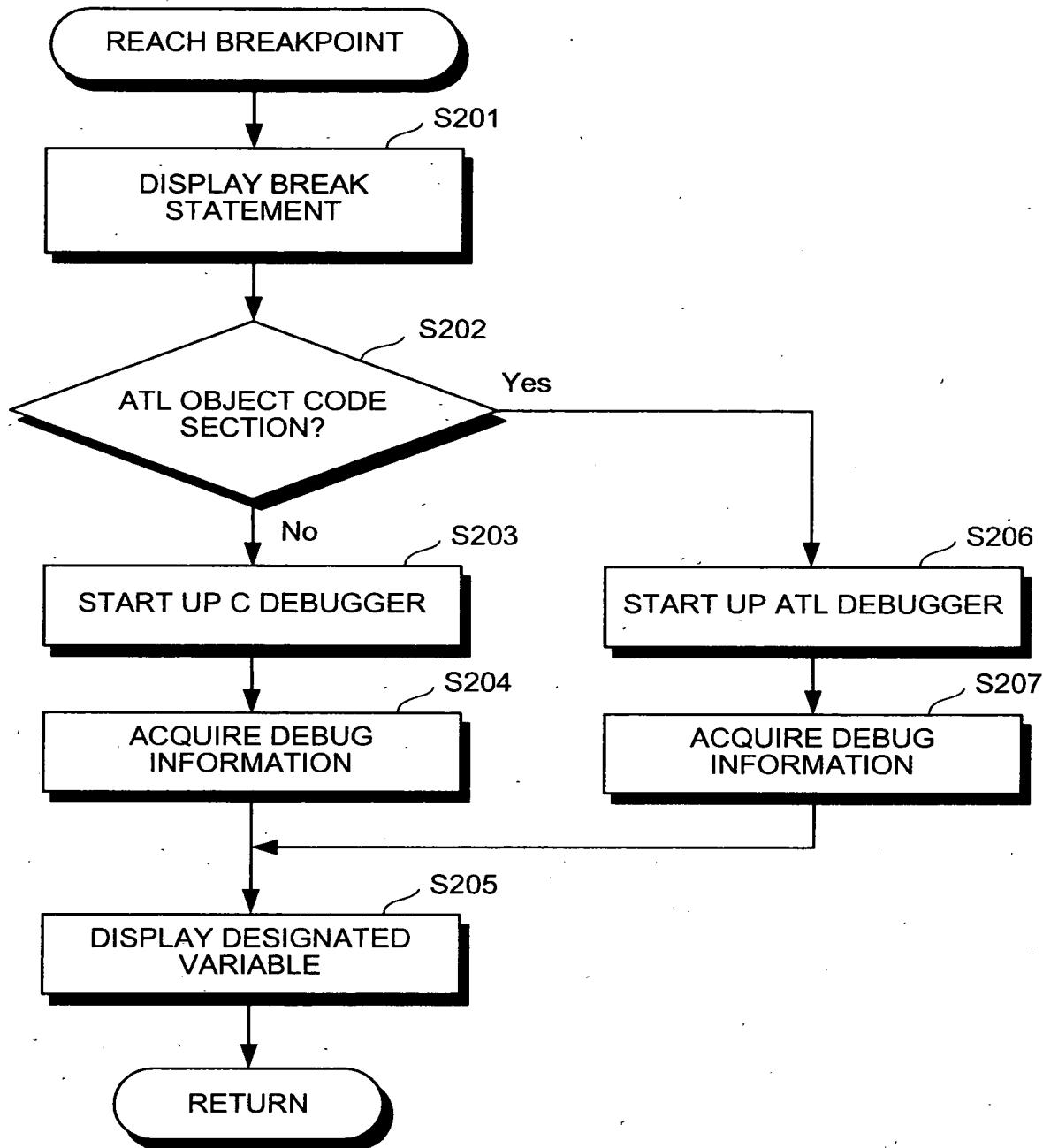


FIG.8

50

File Edit Exec Debug

```

1: #include "AT/hybrid.h"
2:
3: #pragma atl name "SAMPLE"
4: #pragma atl socket "SSOC"
5: #pragma atl proc P1(ARGV1,ARGV2 (2) ) "{
6:   WRITE "ARGV1=",ARGV1, /
7:   WRITE "ARGV2=",ARGV2, /
8: }"
9:
10: int test_main(void)
11: {
12:   int idata;
13:   struct atreal rdata[2];
14:
15:   if(ATL_init2(EM_ATL) != 0){
16:     ATL_perror("ATL_init2 ()");

```

51

52

53

SYMBOL	VALUE
ARGV1	520
ARGV2(0)	25.78
ARGV2(1)	158.14

8/12

FIG. 9.

SYMBOL	VALUE
EM ATL	10

FIG.10

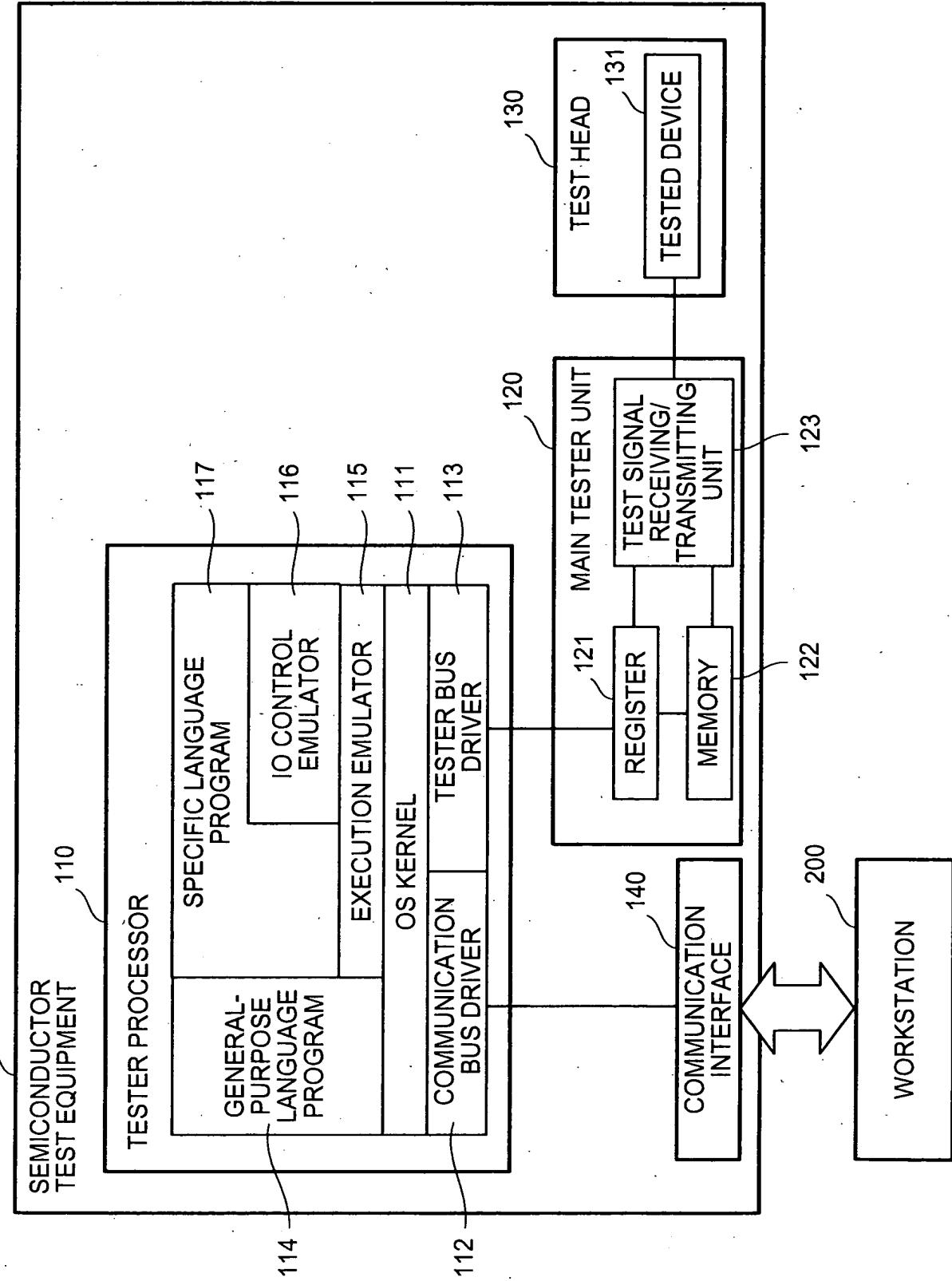


FIG. 1

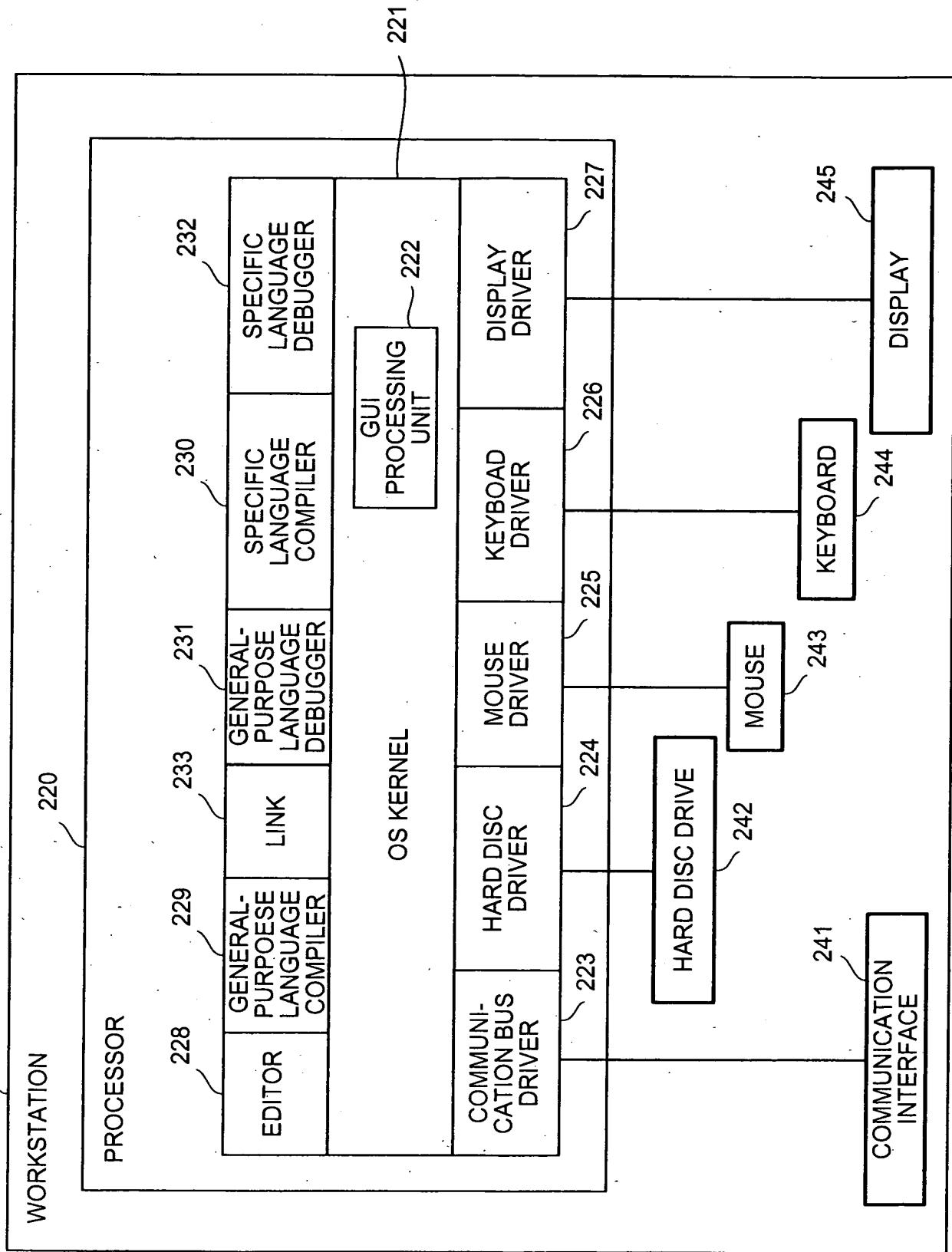


FIG.12

